RC4580 DUAL AUDIO OPERATIONAL AMPLIFIER

SLOS412C - APRIL 2003 - REVISED MARCH 2004

- Operating Voltage . . . ±2 V to ±18 V
- Low Noise Voltage . . . 0.8 μVrms (TYP)
- Wide GBW . . . 12 MHz (TYP)
- Low THD . . . 0.0005% (TYP)
- Slew Rate . . . 5 V/μs (TYP)
- Suitable for Applications Such As Audio Preamplifier, Active Filter, Headphone Amplifier, Industrial Measurement Equipment
- Drop-In Replacement for NJM4580
- Pin and Function Compatible With LM833, NE5532, NJM4558/9, and NJM4560/2/5

description/ordering information

The RC4580 is a dual operational amplifier that has been designed optimally for audio applications, such as improving tone control. It offers low noise, high gain bandwidth, low harmonic distortion, and high output current, all of which make the device ideally suited for audio electronics, such as audio preamplifiers and active filters, as well as industrial measurement equipment. When high output current is required, the RC4580 also can be used as a headphone amplifier. Due to its wide operating supply voltage, the RC4580 also can be used in low-voltage applications.

ORDERING INFORMATION

TA	PACKA	GE†	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	PDIP (P) Tube of 50		RC4580IP	RC4580IP
	0010 (D)	Tube of 75	RC4580ID	D 4500l
-40°C to 85°C	SOIC (D)	Reel of 2500	RC4580IDR	R4580I
	T000D (DIA)	Tube of 150	RC4580IPW	D 45001
	TSSOP (PW)	Reel of 2000	RC4580IPWR	R4580I

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

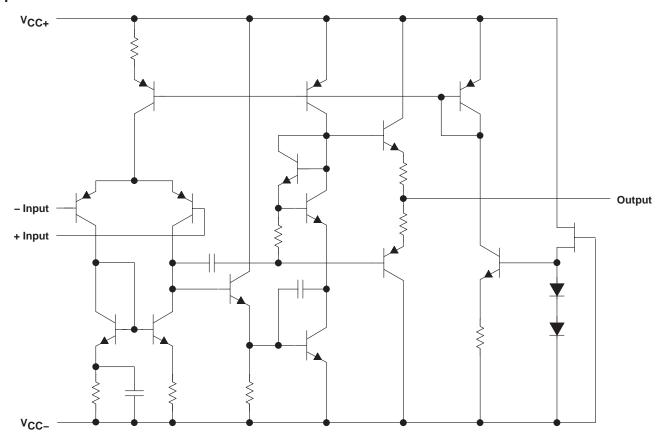


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equivalent schematic



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC+}		±18 V
Input voltage (any input)		±15 V
Differential input voltage, V _{ID}		±30 V
Output current		±50 mA
Package thermal impedance, θ _{JA} (see Notes 1	l and 2): D package	97°C/W
	P package	85°C/W
	PW package	149°C/W
Operating virtual junction temperature, T _J		150°C
Storage temperature range, T _{stg}		–60°C to 125°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		MIN	MAX	UNIT
V _{CC+}	Ourselves Harry	2	16	V
V _{CC} -	Supply voltage	-2	-16	V
VICR	Input common-mode voltage range	-13.5	13.5	V
TA	Operating free-air temperature range	-40	85	°C

electrical characteristics, $V_{CC\pm} = \pm 15 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VIO	Input offset voltage	$R_S \le 10 \text{ k}\Omega$		0.5	3	mV
lio	Input offset current			5	200	nA
I _{IB}	Input bias current			100	500	nA
A _{VD}	Large-signal differential-voltage amplification	$R_L \ge 2 \text{ k}\Omega$, $V_O = \pm 10 \text{ V}$	90	110		dB
Vом	Output voltage swing	$R_L \ge 2 k\Omega$	±12	±13.5		V
VICR	Common-mode input voltage range		±12	±13.5		V
CMRR	Common-mode rejection ratio	R _S ≤ 10 kΩ	80	110		dB
k _{SVR} ‡	Supply-voltage rejection ratio	$R_S \le 10 \text{ k}\Omega$	80	110		dB
Icc	Supply current (all amplifiers)			6	9	mA

[‡] Measured with V_{CC±} varied simultaneously

operating characteristics, $V_{CC\pm}$ = ± 15 V, T_A = $25^{\circ}C$ (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	TYP	UNIT
SR	Slew rate at unity gain	$R_L \ge 2 k\Omega$	5	V/μs
GBW	Gain-bandwidth product	f = 10 kHz	12	MHz
THD	Total harmonic distortion	$V_O = 5 \text{ V}, \text{ R}_L = 2 \text{ k}\Omega, \text{ f} = 1 \text{ kHz}, \text{ A}_{VD} = 20 \text{ dB}$	0.0005%	
Vn	Equivalent input noise voltage	RIAA, R _S \leq 2.2 k Ω , 30-kHz LPF	0.8	μVrms



NOTES: 1. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.

^{2.} The package thermal impedance is calculated in accordance with JESD 51-7.

TYPICAL CHARACTERISTICS

MAXIMUM OUTPUT VOLTAGE SWING LOAD RESISTANCE 15 Maximum Output Voltage Swing - V $V_{CC} \pm = \pm 15 \text{ V}$ 10 T_A = 25°C V_{OM+} 5 0 -5 V_{OM}--10 -15 100 1 k 10 k R_L - Load Resistance - Ω

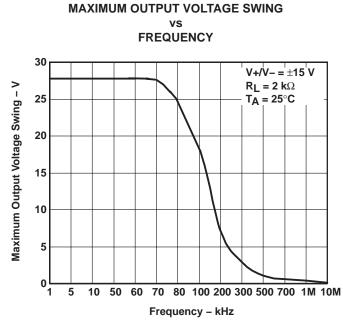
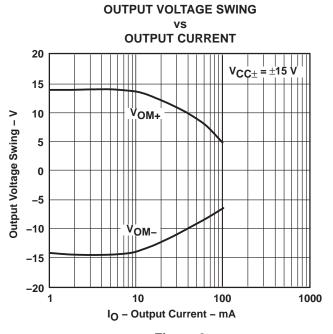


Figure 1

Figure 2



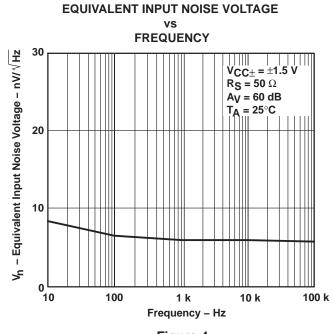
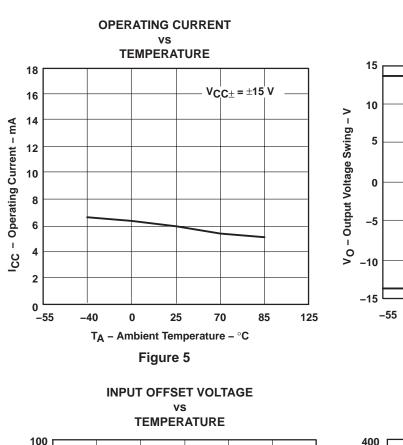
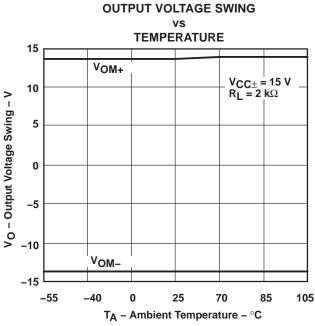


Figure 3

TYPICAL CHARACTERISTICS





100 $V_{CC\pm} = \pm 15 \text{ V}$ V_{IO} – Input Offset Voltage – μV 50

0

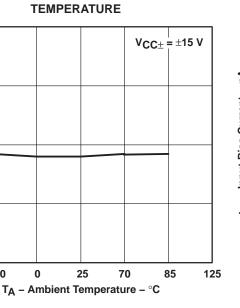
-50

-100 -50

-40

INPUT BIAS CURRENT

Figure 6



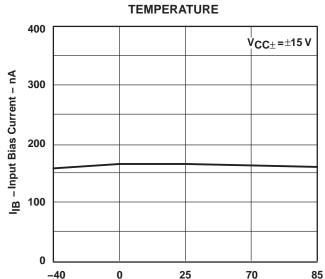


Figure 7

25

70

Figure 8

T_A - Ambient Temperature - °C

TYPICAL CHARACTERISTICS

MAXIMUM OUTPUT VOLTAGE SWING OPERATING VOLTAGE 20 $T_A = 25^{\circ}C$ V_O - Maximum Output Voltage Swing - V $R_L = 2 k\Omega$ 15 +VOM 10 5 -5 -10 -VOM -15 -20 0 ±5 ±10 ±15 ±20 V_{CC+}/V_{CC-} – Operating Voltage – V



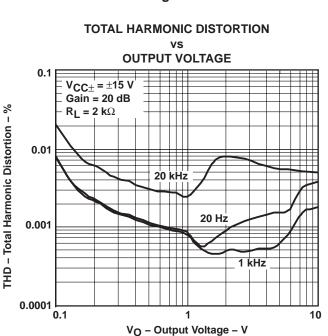


Figure 11

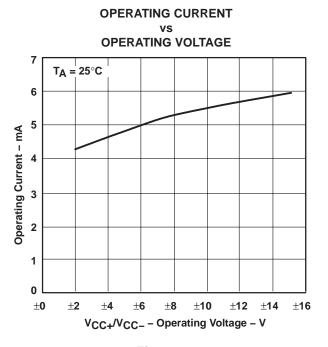


Figure 10

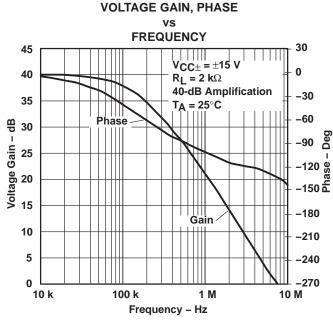


Figure 12









PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
RC4580ID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IDR	ACTIVE	SOIC	D	8	2500 G	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
RC4580IPE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
RC4580IPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IPWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4580IPWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PACKAGE OPTION ADDENDUM

18-Jul-2006

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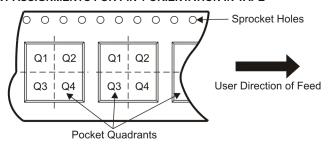
TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
RC4580IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
RC4580IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
RC4580IPWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1





*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
RC4580IDR	SOIC	D	8	2500	340.5	338.1	20.6
RC4580IDR	SOIC	D	8	2500	346.0	346.0	29.0
RC4580IPWR	TSSOP	PW	8	2000	346.0	346.0	29.0

PW (R-PDSO-G**)

14 PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

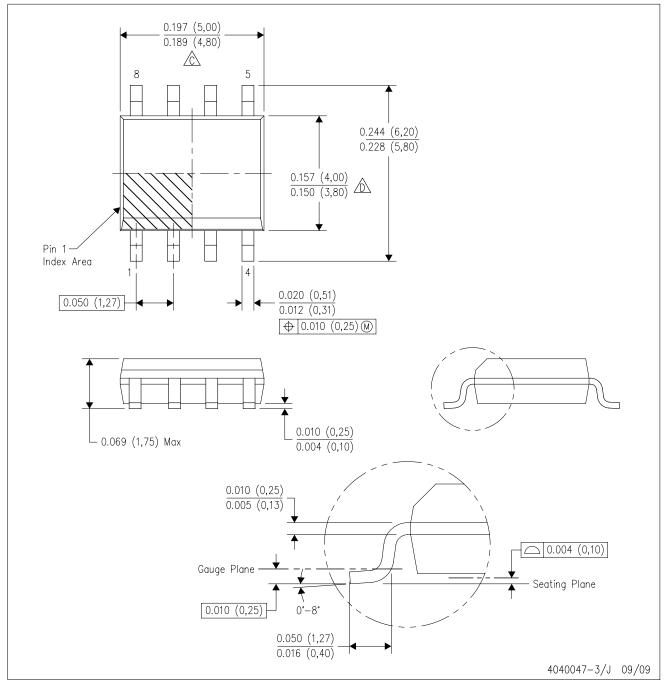
B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AA.



P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

For the latest package information, go to http://www.ti.com/sc/docs/package/pkg_info.htm

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